



Attorney Docket No. 826.1597

*cfc*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Ryuichi Sunayama, et al.

Application No.: 09/533,042

ATTENTION

Confirmation No.: 7804

CERTIFICATE OF CORRECTION

Filed: March 22, 2000

BRANCH

U.S. Patent No.: 6,898,698

Batch No.:

Issued: May 24, 2005

For: DEVICE PREDICTING A BRANCH OF AN INSTRUCTION EQUIVALENT TO A  
SUBROUTINE RETURN AND A METHOD THEREOF

**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**Certificate**

**MAY 04 2006**

**of Correction**

Sir:

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s) shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date: 5/1/6

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**MAY 05 2006**

## UNITED STATES PATENT AND TRADEMARK OFFICE

# CERTIFICATE OF CORRECTION

PATENT NO : 6,898,698

DATED : May 24, 2005

INVENTOR(S) : Ryuichi Sunayama, et al.

Col. 18, line 29, insert --a-- after "only".

Col. 18, line 54, change "recognize" to --recognized--.

Col. 19, line 51, change "fore" to -- for a--.

Col. 20, line 24, change "specking" to --specifying--.

Col. 21, line 17, change "cell" to --call--.

Col. 22, line 2, insert --is-- after "return".

Col. 22, line 5, change "Information" to --information--.

Col. 22, line 52, change ":" to --;--.

Col. 24, line 5, delete "comparing" after "circuit" and insert --make a comparison between--.

Col. 24, line 9, change "said" to --the--; and after "a" insert --result of the--.

Col. 24, line 10, delete "result".

Col. 24, line 13, delete lines 13-23 and insert --an identifying circuit identifying an instruction equivalent to a subroutine return, which corresponds to the instruction equivalent to the subroutine call, based on the result of the comparison

wherein the predicted branch is the branch of the instruction equivalent to the subroutine return in an architecture not including a particular instruction for a subroutine return,

wherein the storing circuit stores the return address of the subroutine as the information specifying the return address, and--.

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PATENT NO. 6,898,698

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